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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE

Docket No.: 303.324US2

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Commissioner for Patents
Washington, D.C. 20231



We are transmitting herewith the following attached items (as indicated with an "X"):

- A return postcard.
- An Amendment and Response (12 Pages).
- A Supplemental Information Disclosure Statement (1 pgs.), Form 1449 (1 pgs.), and copies of 11 cited references.
- A check in the amount of \$240.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 5 day of October, 2000.

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